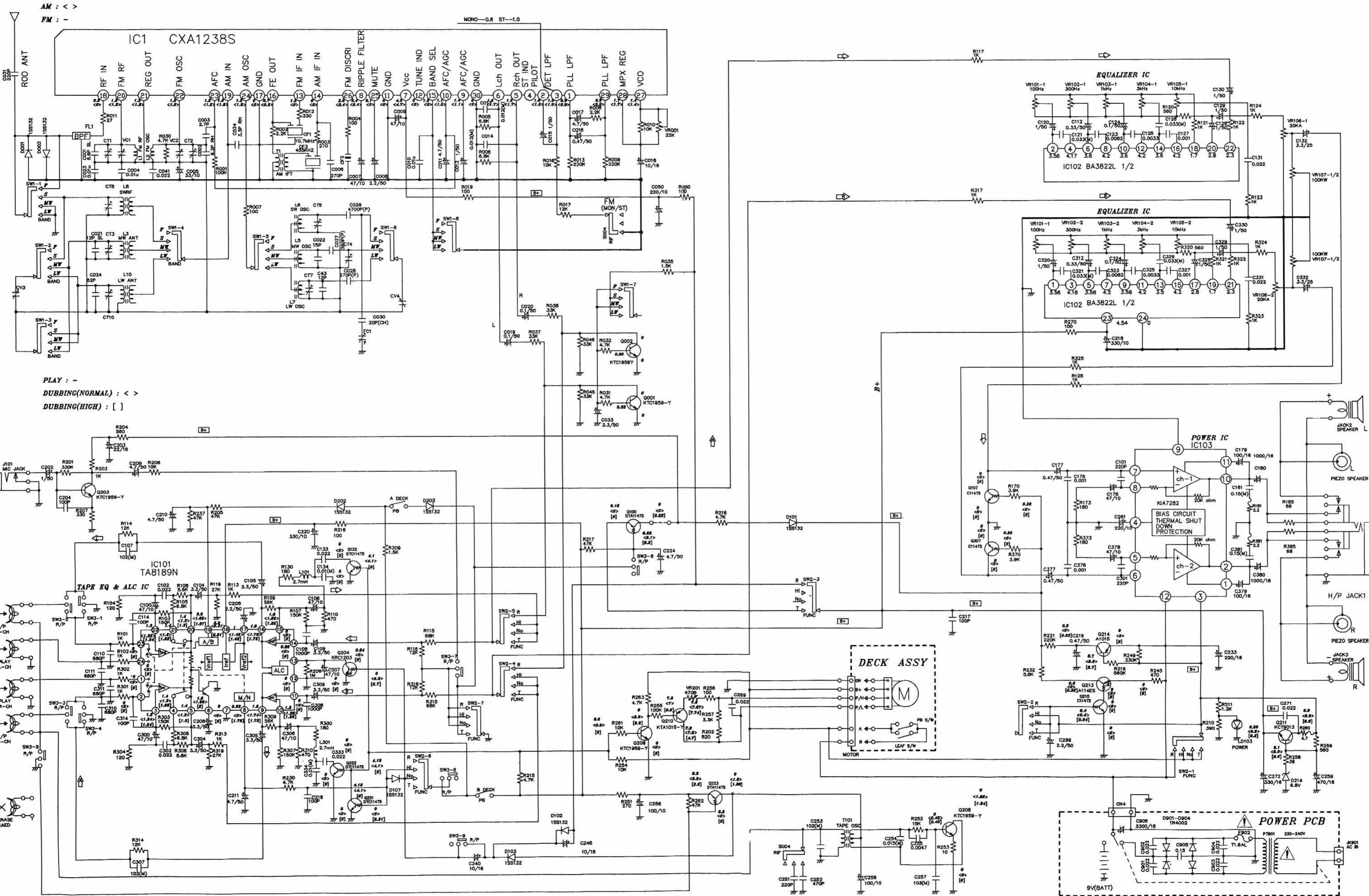


SONY CFS-W 430L SCHEMATIC DIAGRAM

Note: Design and specifications subject to change without notice.



AM : <>
FM : -

PLAY : -
DUBBING(NORMAL) : <>
DUBBING(HIGH) : []

■ Note on Schematic Diagram → P/N 495-031N

IC101 TA8189N LOGIC STATES

PIN 8	HIGH	LOW
4-5, 20-21 ON	4-5, 20-21 OFF	
PIN 19	PIN 24/1 INPUT	PIN 23/2 INPUT

- All capacitors are in μF unless otherwise noted pF (or μM F) 50WV or less are not indicated except for electrolytics and tantalums.
 - Voltages are taken with a VOM (50k Ω /V).
 - Voltage variations may be noted due to normal production to tolerances.
 - AC Voltage readings in the bias oscillator with a VTVM.
 - Total current measured with no cassette installed.
- Note: The component identified by mark Δ or dotted line with Δ are critical for safety. Replace only with part number specified.